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Intel® Arc™ A750 Graphics



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Essentials

Product Collection	Intel® Arc™ A-Series Graphics
Model Number	A750
Code Name	Products formerly Alchemist
Microarchitecture	Xe HPG
Lithography Type	TSMC N6
Vertical Segment	Desktop
Marketing Status	Launched
Launch Date ⓘ	Q3'22
Embedded Options Available ⓘ	No

GPU Specifications

X ^e -cores	28
Render Slices	7
Ray Tracing Units	28
Intel® X ^e Matrix Extensions (Intel® XMV) Engines	448
X ^e Vector Engines	448
Graphics Clock ⓘ	2050 MHz
TBP ⓘ	225 W
PCI Express Configurations † ⓘ	Up to PCI Express 4.0 x16

Device ID	0x56A1
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Memory Specifications

Memory Size	8 GB
Memory Type	GDDR6
Graphics Memory Interface	256 bit
Graphics Memory Bandwidth	512 GB/s
Graphics Memory Speed	16 Gbps

Supported Technologies

Ray Tracing	Yes
oneAPI Support ⓘ	Yes
OpenVINO™ Support ⓘ	Yes
DirectX* Support ⓘ	DirectX 12 Ultimate
Vulkan* Support	1.3
OpenGL* Support ⓘ	Up to 4.6
OpenCL* Support ⓘ	3.0
Multi-Format Codec Engines ⓘ	2
Adaptive Sync	Yes

I/O Specifications

# of Displays Supported†	4
Graphics Output†	eDP* 1.4, DP 2.0 up to UHBR 10**, HDMI* 2.1, HDMI* 2.0b
Max Resolution (HDMI)† ⓘ	4096 x 2160@60Hz
Max Resolution (DP)† ⓘ	7680 x 4320@60Hz

Features

H.264 Hardware Encode/Decode	Yes
H.265 (HEVC) Hardware Encode/Decode	Yes
AV1 Encode/Decode	Yes
VP9 Bitstream & Decoding	Yes

Intel® Deep Link Technologies

Intel® Deep Link Hyper Compute†	Yes
Intel® Deep Link Hyper Encode†	Yes
Intel® Deep Link Stream Assist†	Yes

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Refer to Datasheet for formal definitions of product properties and features.

‡ This feature may not be available on all computing systems. Please check with the system vendor to determine if your system delivers this feature, or reference the system specifications (motherboard, processor, chipset, power supply, HDD, graphics controller, memory, BIOS, drivers, virtual machine monitor-VMM, platform software, and/or operating system) for feature compatibility. Functionality, performance, and other benefits of this feature may vary depending on system configuration.

"Announced" SKUs are not yet available. Please refer to the Launch Date for market availability.

**Designed for DP2.0, certification pending VESA CTS Release.

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